FIG. 1   PRIOR ART

Approved
PT
2/2/0
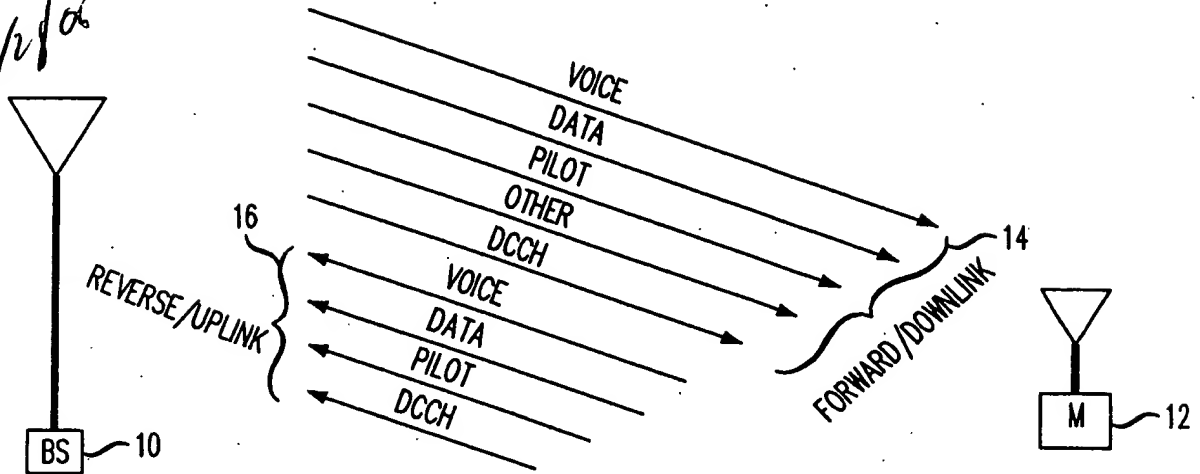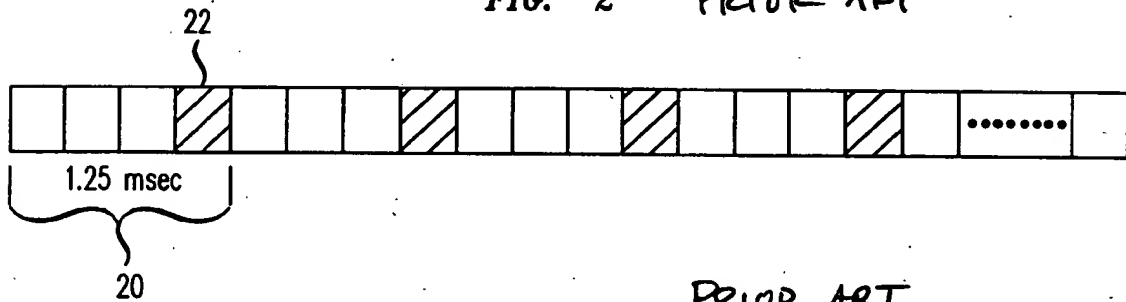

FIG. 2   PRIOR ART

1.25 msec

FIG. 3   PRIOR ART

FIG. 4   PRIOR ART

VOICE
DATA
PILOT
PILOT
VOICE
DATA

VOICE
PILOT
PILOT
VOICE

BS — 10

M — 12

50 — BS

FIG. 5

74

68   70   72

1.25 msec

60   62   64   66

FIG. 6

74

80   82   84   86   88   90   92   94

FIG. 7
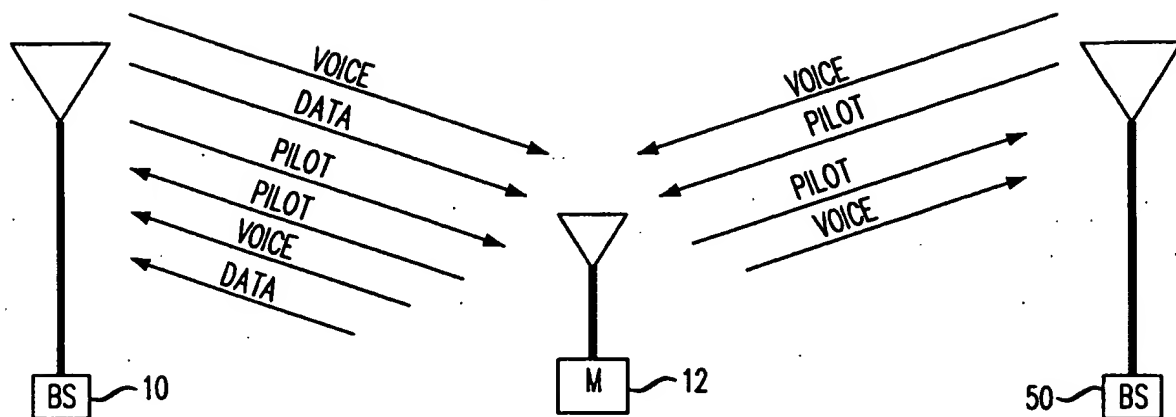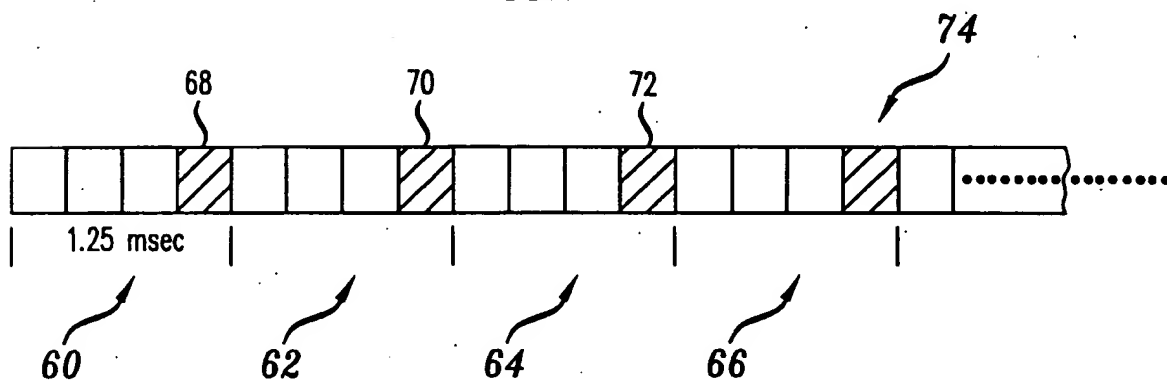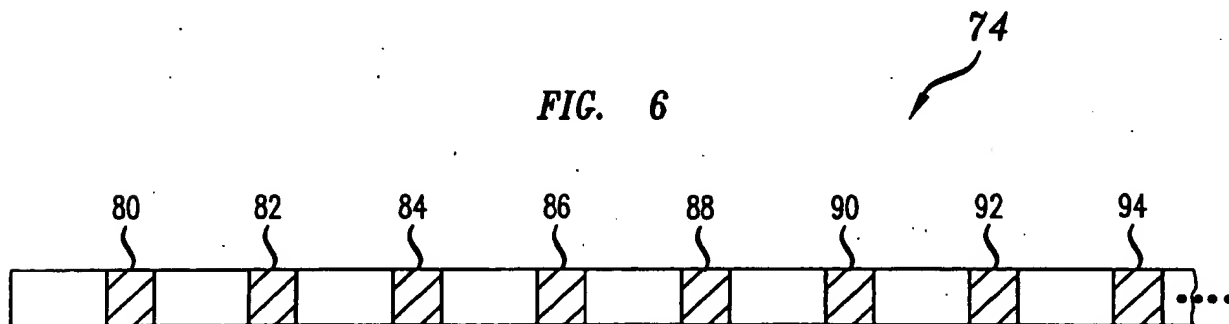
CONFIGURATION NAME	POWER CONTROL BIT PATTERN (EXAMPLE)			
V50 D750	VDDD	DDDD	DDDD	DDDD
V100 D700	VDDD	DDDD	VDDD	DDDD
V150 D650				
V200 D600	VDDD	VDDD	VDDD	VDDD
V250 D550				
V300 D500	VDVD	VDDD	VDVD	VDDD
V350 D450				
V400 D400	VDVD	VDVD	VDVD	VDVD
⋮				
V750 D50	V D V V	V D V V	V D V V	V D V V

V ≡ VOICE

D ≡ DATA

FIG. 8

| t → | ← ACTION → TIME | | | | | | | |

MOBILE RECEIVES DATA BURST

MOBILE PROCESS DATA BITS AND ASSIGNED WALSH CODE

FIRST REVERSE LINK FRAME BOUNDARY